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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/671,843	09/27/2000	Naoaki Komiya	YKI-0049	6716

7590 09/11/2002

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EXAMINER

NGUYEN, KIMNHUNG T

ART UNIT

PAPER NUMBER

2674

DATE MAILED: 09/11/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)
	09/671,843	KOMIYA ET AL.
	Examiner	Art Unit
	Kimnhung Nguyen	2674

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.

- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.

- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.

- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).

- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1) Responsive to communication(s) filed on _____.

2a) This action is FINAL. 2b) This action is non-final.

3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4) Claim(s) 1-5 is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5) Claim(s) _____ is/are allowed.

6) Claim(s) 1-5 is/are rejected.

7) Claim(s) _____ is/are objected to.

8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9) The specification is objected to by the Examiner.

10) The drawing(s) filed on _____ is/are: a) accepted or b) objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

11) The proposed drawing correction filed on _____ is: a) approved b) disapproved by the Examiner.

If approved, corrected drawings are required in reply to this Office action.

12) The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. §§ 119 and 120

13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All b) Some * c) None of:

1. Certified copies of the priority documents have been received.

2. Certified copies of the priority documents have been received in Application No. _____.

3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).

a) The translation of the foreign language provisional application has been received.

15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

Attachment(s)

1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)	4) <input type="checkbox"/> Interview Summary (PTO-413) Paper No(s). _____ .
2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)	5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)
3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449) Paper No(s) _____ .	6) <input type="checkbox"/> Other: _____ .

DETAILED ACTION

This Application has been examined. The original claims 1-5 are pending. The examination results are as following.

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 1-2 are rejected under 35 U.S.C. 103(a) as being unpatentable over Prior Art figure 3 admitted by Applicant in view of Osada et al. (US patent 5,973,456).

3. Regarding claims 1-2, Prior Art figure 3 discloses that an active matrix type electroluminescence display device comprising a plurality of display pixels (GS1-GS3) arranged in rows and columns in a matrix form; gate signal line (15) which is connected to and shared by a plurality of display pixels provided on each row; and gate drive circuit for sequentially supplying select signal to the gate signal line (15); wherein each of the display pixels includes an electroluminescence element (11); a first thin film transistor (12) in which a display signal is applied to the drain and which is switched on and off in response to the select signal, and a

second thin film transistor for driving the electroluminescence element (11) based on the display signal; and the gate drive circuit is supplied from both end of the gate signal lined to the signal line. However, figure 3 does not disclose a plurality of gate lines, each of which is connected to and shared by a plurality of display pixels provided on each row; and gate drive circuits are placed from both ends of the gate signal lines to the lines signal, and the gate drive circuits include a first and second gate drive. Osada et al. disclose in figure 1 that a plurality of gate lines (2, 3), each of which is connected to and shared by a plurality of display pixels provided on each row; and therefore, gate drive circuits are placed from both ends of the gate signal lines to the lines signal, and the gate drive circuits include a first and second gate drive circuits (2, 3, see column 3, lines 40-42) arranged in a symmetric pattern to the right and left of the display portion. It would have been obvious to one of ordinary skill in the art at the time the invention was made to have disclose plurality of gate lines as taught by Osada et al. in the device of Prior Art of figure 3 because this would perform display drive for EL panel and connect to other scan electrodes are all the switched on/off state.

4. Claims 3-5 are rejected under 35 U.S.C. 103(a) as being unpatentable over Prior Art figure 3 admitted by Applicant as applied to claims 1-2 above, and further in view of Peng et al. (US patent 6,078,142).

5. Claims 3-5 are dependent upon claims 1-2, and are rejected on the same reasons set forth in claims 1-2 and by the rationale noted above. Furthermore, Prior Art and Osada et al. do not disclose that each of the first and second gate drive circuits includes a plurality of shift registers

for sequentially shifting a reference clock with a pulse width of one horizontal period; and buffer amplifiers for driving of gate signal lines based on the output of the shift registers. Peng et al. disclose in figure 4 that gate drive circuits includes a plurality of shift registers (610) for sequentially shifting a reference clock (612) with a pulse width of one horizontal period; and output driver (630a-630d), they may also be buffer amplifiers (630a-630d) for driving of gate signal lines based on the output of the shift registers (see figure 4, column 4, lines 3-23). It would have been obvious to one of ordinary skill in the art at the time the invention was made to utilize the teaching of Peng et al. as discussed above in the device of figure 3 of Prior art and Osada et al. for producing the claimed invention because this would provide the attachment to the voltage source and make correction potential to the emission of the electrons.

Correspondence

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kimnhung Nguyen whose telephone number (703) 308-0425.

If attempts to reach the examiner by telephone are unsuccessfully, the examiner's supervisor, **RICHARD A HJERPE** can be reached on (703) 305-4709.

Any response to this action should be mailed to:

Commissioner of Patents and Trademarks

Washington, D. C. 20231

Or faxed to:

(703) 872-9314 (for Technology Center 2600 only).

Hand-delivery response should be brought to: Crystal Park II, 2121 Crystal Drive,
Arlington, VA Sixth Floor (Receptionist).

Any inquiry of a general nature or relating to the status of this application or proceeding
should be directed to the Technology Center 2600 Customer Service Office whose telephone
number is (703) 306-0377.

Kimnhung Nguyen
August 26, 2002



RICHARD HJELPE
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2600